

SERVER CHASSIS HARDWARE MASTER  
SYSTEM AND METHOD

ABSTRACT OF THE DISCLOSURE

A system and method for selecting a hardware master from a plurality of computing devices and controlling the communication of a master control signal(s) to one or more of the plurality of computing devices includes monitoring first and second connectors coupled with a midplane to detect the presence of first and second computing devices, respectively. Master control signals may be transmitted to the second computing device if the first computing device is not coupled with the first connector. Transmission of the master control signals to the second computing devices may be prevented if the first computing device is coupled with the first connector. In accordance with a particular embodiment of the present invention, master control signals may be transmitted to the first computing device if the first computing device is coupled with the first connector.

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